

# Alinx AXU2CGB

## Xilinx Zynq XCZU2CG-1SFVC784E

Interface and Function	
DP Interface	Mini Display Port up to 4K@30Hz or 1080P@60Hz Output (ALINX customized DP to HDMI cable, need to buy Separately )
PCIE Interface	Used to Connect PCIE Peripherals, Communication Speed up to 5Gbps
MIPI Interface x 2	Used to Connect ALI NX MI PI Interface OV5640 Camera Mod u le AN5641
USB 3.0 x 4	Used to Connect External USB Peripherals
USB Uart	Used for Serial Communication with PC or External Devices
Gigabit Ethernet	10/100/1000M Ethernet with RJ-45 Interfaces for Data Exchange
System Clock	PS System Clock is 33.3333Mhz, PL Clock is 25Mhz (Not with Battery)
JTAG	10-pin 0.1-inch Standard JTAG Port for Programs Debug and Download
EEPROM	EEPROM 24LC04 with I IC Interface On-Board
SD Card Slot	1 Micro SD Card Slot, Support SD Mode and SPI Mode
LED	5 User LEDs
KEYs	5 KEYs, 1 Reset KEY, 4 User KEYs
40-Pin Expansion Ports	Two 40-Pin Expansion Ports (0.1 inch Pitch), Can be Connect with Various ALI NX Modules (Binocular Camera Module, TFT LCD Screen, Camera, AD/DA and Other Modules).

FPGA Board	AXU2CGA	AXU2CGB
FPGA Chip	XCZU2CG-1SFVC784E	XCZU2CG-1SFVC784E
Chip CPU	Dual-Core ARM Cortex-A53, 1.2GHz, Dual-Core ARM CortexTm-R5, 500MHz	Dual-Core ARM Cortex-A53, 1.2GHz, Dual-Core ARM CortexTm-R5, 500MHz
PS RAM	1GB 32bit DDR4 2400Mbps	<b>2GB 64bit</b> DDR4 2400Mbps
Chip Level	Commercial Grade	Commercial Grade
Working Temperature	0°C to 70°C	0°C to 70°C
Speed Grades	-1	-1
Logic Cells	103K	103K
CLB Flip-Flops	94K	94K
Look Up Tables (LUTs)	47K	47K
Global Clock	3	3
Multiplier	240	240
MAX. DISTRIBUTED RAM	1.2Mb	1.2Mb
BLOCK RAM	5.3Mb	5.3Mb
EMMC FLASH	NA	<b>8GB</b>
QSPI FLASH	32MB	32MB
HP I/O (1.8V)	34	34
HD I/O (3.3V)	34	34